Attorney's Docket No.: 07977-Applicant : Shunper Yamazaki et al. 218003 / US3531/3615D1D1

Serial No.: 10/753,524 Filed : January 9, 2004 Page : 2 of 8

Amendments to the Claims

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims

1-20. (Canceled)

21. (Previously Presented) A personal computer comprising:

a semiconductor film provided over a substrate and comprising a source region, a drain region and a channel formation region provided between said source region and said drain region; and

a gate electrode provided adjacent to said channel formation region with a gate insulating film therebetween.

wherein lattices are continuously connected to each other at a grain boundary of said semiconductor film.

- 22. (Previously Presented) A computer according to claim 21 further comprising an auxiliary capacitance.
 - 23. (Previously Presented) A computer according to claim 21 further comprising:

a pixel electrode:

an opposite electrode; and

a liquid crystal provided between said pixel electrode and said opposite electrode.

24. (Canceled)

25. (Previously Presented) A computer according to claim 21 wherein channel length of said channel formation region is 2 µm or shorter.

 Applicant
 : Shunpei Yamazaki et al.
 Attorney's Docket No.: 07977

 Serial No.: 10:753,524
 218003 / US3531/3615D1D1

Filed : January 9, 2004

Page : 3 of 8

26-41. (Canceled)

42. (Previously Presented) A computer according to claim 21 wherein direction of movement of a carrier in said channel formation region coincides with direction of extension of said grain boundary.

- 43. (New) A computer according to claim 21 wherein the semiconductor film comprises silicon.
- (New) A computer according to claim 21 wherein the semiconductor film comprises a rod-shaped crystal.
- 45. (New) A computer according to claim 21 wherein the semiconductor film comprises a flattened rod-shaped crystal.
 - 46. (New) A computer according to claim 23 wherein the pixel electrode comprises ITO.
 - 47. (New) A personal computer comprising:
- a semiconductor film provided over a substrate and comprising a source region, a drain region and a channel formation region provided between said source region and said drain region; and
- a gate electrode provided adjacent to said channel formation region with a gate insulating film therebetween, and
- a thermal oxidation film provided between the semiconductor film and the gate electrode, wherein lattices are continuously connected to each other at a grain boundary of said semiconductor film.
 - 48. (New) A computer according to claim 47 further comprising an auxiliary capacitance.

 Applicant
 : Shunpei Yamazaki et al.
 Attorney's Docket No.:
 07977

 Serial No.;
 10/753,524
 218003 / US3531/3615D1D1

Filed: January 9, 2004

Page : 4 of 8

49. (New) A computer according to claim 47 further comprising:

a pixel electrode;

an opposite electrode; and

a liquid crystal provided between said pixel electrode and said opposite electrode.

 (New) A computer according to claim 47 wherein channel length of said channel formation region is 2 μm or shorter.

- 51. (New) A computer according to claim 47 wherein direction of movement of a carrier in said channel formation region coincides with direction of extension of said grain boundary.
- 52. (New) A computer according to claim 47 wherein the semiconductor film comprises silicon.
- (New) A computer according to claim 47 wherein the semiconductor film comprises a rod-shaped crystal.
- (New) A computer according to claim 47 wherein the semiconductor film comprises a flattened rod-shaped crystal.
 - 55. (New) A computer according to claim 49 wherein the pixel electrode comprises ITO.
 - 56. (New) A personal computer comprising:
- a semiconductor film provided over a substrate and comprising a source region, a drain region, a channel formation region provided between said source region and said drain region, and a low concentration impurity region provided between the channel formation region and at least one of the source region and the drain region; and

 Applicant
 : Shunpel Yamazaki et al.
 Autorney's Docket No.: 07977

 Serial No.: 10/753,524
 218003 / US3531/3615D1D1

Filed : January 9, 2004

Page : 5 of 8

a gate electrode provided adjacent to said channel formation region with a gate insulating film therebetween.

wherein lattices are continuously connected to each other at a grain boundary of said semiconductor film.

- 57. (New) A computer according to claim 56 further comprising an auxiliary capacitance.
- 58. (New) A computer according to claim 56 further comprising:
- a pixel electrode;
- an opposite electrode; and
- a liquid crystal provided between said pixel electrode and said opposite electrode.
- 59, (New) A computer according to claim 56 wherein channel length of said channel formation region is 2 μm or shorter.
- 60. (New) A computer according to claim 56 wherein direction of movement of a carrier in said channel formation region coincides with direction of extension of said grain boundary.
- (New) A computer according to claim 56 wherein the semiconductor film comprises silicon.
- (New) A computer according to claim 56 wherein the semiconductor film comprises a rod-shaped crystal.
- 63. (New) A computer according to claim 56 wherein the semiconductor film comprises a flattened rod-shaped crystal.
 - 64. (New) A computer according to claim 58 wherein the pixel electrode comprises ITO.